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(54) **Solid-state display with reference pixel**

(57) A system for controlling a digital image display device having addressable pixels on a substrate, the pixels having performance attributes, and a control circuit for controlling the pixels of the display device, including: a reference pixel located on the substrate and connected to the control circuit, the reference pixel having the same performance attributes as the pixels in the display; a measurement circuit connected to the refer-

ence pixel to produce an output signal representative of the performance attributes of the reference pixel; an analysis circuit connected to the measurement circuit to receive the output signal, compare the performance attributes with predetermined performance attributes, and produce a feedback signal in response thereto; and the control circuit being adapted to receive the feedback signal and control the digital solid-state display device in response thereto.

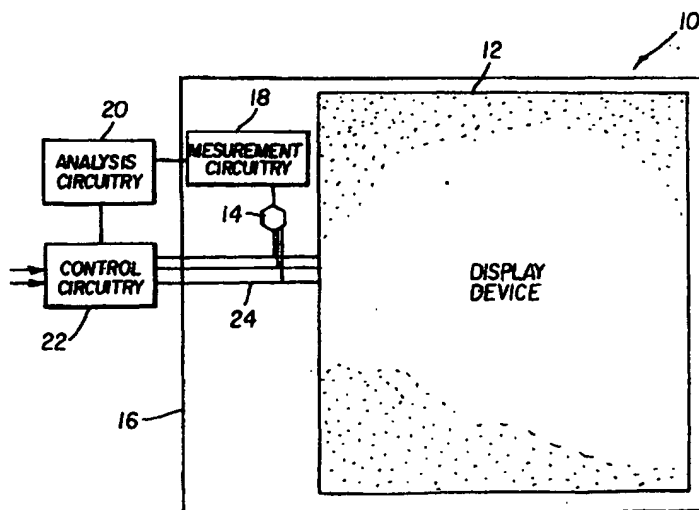


FIG. 1



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EUROPEAN SEARCH REPORT

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EP 01 20 1792

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The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
Place of search MUNICH		Date of completion of the search 11 December 2002	Examiner Wolff, L
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	



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Application Number
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**ANNEX TO THE EUROPEAN SEARCH REPORT
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